

REMARKS

Claims 1 and 3-20 are now pending in the application. Claim 1 has been amended. Support for the foregoing amendments may be found throughout the written description, drawings and claims, as originally filed.

Applicant has thoroughly reviewed the outstanding Office Action including the Examiner's remarks and the references cited therein. The Examiner is respectfully requested to reconsider and withdraw the rejections in view of the amendments and remarks contained herein.

Drawings

Applicants note that the Examiner has not yet acknowledged receipt of the drawings and acceptance of them as in compliance with USPTO requirements. Such acknowledgement and acceptance is respectfully requested.

REJECTION UNDER 35 U.S.C. § 102

Claims 1-20 are rejected under 35 U.S.C. § 102(b) as being anticipated by Mick et al. (US 5,751,638). This rejection is traversed for at least the following reasons.

“For a prior art reference to anticipate a claim under 35 U.S.C. § 102, every element of the claimed invention must be identically shown in a single reference.” *Diversitech Corp. v. Century Steps, Inc.*, 850 F.ed 675, 677, 7 USPQ 2d 1315, 1317 (Fed. Cir. 1988).

Independent Claims 1, 6, 9, 12, 15 and 18 recite an easy access ports structure. In accordance with the present invention, each port has a register bank. Each register bank has the same address. A global register is used to store the status of register bank. When operating, a CPU accesses one port in accordance with the application program. The status values of the other ports are mapped to the global register. Therefore, the CPU also can understand the other ports status through the global register when accessing one port. According to the claimed invention, each port has the same address. In other words, the present invention provides an access structure that a device (CPU) can access a plurality of register bank. Moreover, the main purpose of the claimed invention is to resolve the problem of that additional address lines are

required for creating additional addresses to store the status of these port when a device with a plurality of port.

The Mick reference does not disclose the above-recited features as discussed below.

The Examiner asserts that left and right ports of the Mick reference correspond to the claimed first and second ports. However, in the Mick reference, the left and right ports of a memory device 201 are communicated with the left and right resource sharing devices 205 and 206. In other words, the Mick reference discloses two devices 205 and 206 to share a memory device 201, which is different the claimed invention that provides a device to access a plurality of memory locations. Moreover, the Examiner also asserts that semaphore logic control circuit 302 of the Mick reference corresponds to the claimed global register. However, the semaphore logic circuit in the Mick reference just arbitrates on a first received basis between access requests from the left and right electronic devices (col. 4, lines 40-42 and col. 5, lines 19-22). The semaphore logic control circuit 302 does not store any status value of the left and right ports, as global register in the claim 1 of the present application. Therefore, in the cited reference, the sharing devices 205 and 206 may not monitor the other ports status through the semaphore logic control circuit 302 when accessing one port to let abnormal statuses of the other ports eliminated in advance for improving the data processing speed.

In view of the above, Mick does not disclose the above-recited features of the claimed invention. Thus, the Mick reference does not show every element of the claimed invention. Applicant respectfully submits that Claims 1, 6, 9, 12, 15 and 18 are allowable over Mick and respectfully requests the 35 U.S.C. § 102(b) rejection of claims 1, 6, 9, 12, 15 and 18 to be reconsidered and withdrawn.

Claims 3-5, 7-8, 10-11, 13-14, 16-17 and 19-20 depend from base Claims 1, 6, 9, 12, 15 and 18 respectively, and further define additional technical features of the present invention. In view of the patentability of the base claim, and in further view of the additional technical features, Claims 3-5, 7-8, 10-11, 13-14, 16-17 and 19-20 are patentable over the cited reference, the 35 U.S.C. § 102(b) rejection of these claims should be withdrawn as well.

Reconsideration and withdrawal of this rejection is respectfully requested.

Amendment under 37 C.F.R. § 1.111
Application No. 10/630,898

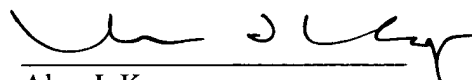
CONCLUSION

It is believed that all of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider and withdraw all presently outstanding rejections. It is believed that a full and complete response has been made to the outstanding Office Action, and as such, the present application is in condition for allowance. Thus, prompt and favorable consideration of this amendment is respectfully requested.

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,



Alan J. Kasper
Registration No. 25,426

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

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